

4 / Priority
Claim

E. Usillio
9-19-02

501.37226VX1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: ITABASHI, et al.

Filed: September 25, 2001

For: SEMICONDUCTOR DEVICE HAVING WIRES AND INSULATOR
LAYERS WITH VIA-STUDS

Je973 U.S. PTO

09/26/2001

09/25/01

CLAIM FOR PRIORITY

Assistant Commissioner for Patents
Washington, D.C. 20231

September 25, 2001


Sir:

Pursuant to the provisions of 35 USC §119 and 37 CFR
§1.55, Applicants hereby claim the right of priority based on
Japanese Patent Application No. 10-143133, filed in Japan on
May 25, 1998.

A certified copy of the above-referred-to Japanese Patent
Application was submitted in prior application Serial No.
09/317,955, filed May 25, 1999, on May 25, 1999.

Respectfully submitted,

ANTONELLI, TERRY, STOUT & KRAUS, LLP



William I. Solomon
Registration No. 28,565

1300 North Seventeenth Street
Suite 1800
Arlington, VA 22209
Tel.: 703-312-6600
Fax.: 703-312-6666

WIS/slk